Designer's™ Data Sheet

Insulated Gate Bipolar Transistor

N-Channel Enhancement-Mode Silicon Gate

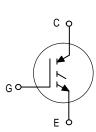
This Insulated Gate Bipolar Transistor (IGBT) uses an advanced termination scheme to provide an enhanced and reliable high voltage–blocking capability. Short circuit rated IGBT's are specifically suited for applications requiring a guaranteed short circuit withstand time such as Motor Control Drives. Fast switching characteristics result in efficient operation at high frequencies.

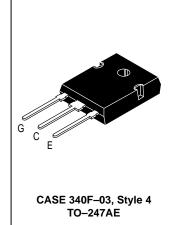
- Industry Standard High Power TO–247 Package with Isolated Mounting Hole
- High Speed E_{off}: 60 μJ per Amp typical at 125°C
- High Short Circuit Capability 10 μs minimum
- Robust High Voltage Termination
- Robust RBSOA



Motorola Preferred Device

IGBT IN TO-247
30 A @ 90°C
50 A @ 25°C
600 VOLTS
SHORT CIRCUIT RATED





MAXIMUM RATINGS (T_C = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit	
Collector–Emitter Voltage	VCES	600	Vdc	
Collector–Gate Voltage (R _{GE} = 1.0 MΩ)	VCGR	600	Vdc	
Gate-Emitter Voltage — Continuous	VGE	±20	Vdc	
Collector Current — Continuous @ T _C = 25°C — Continuous @ T _C = 90°C — Repetitive Pulsed Current (1)	I _{C25} I _{C90} I _{CM}	50 30 100	Adc Apk	
Total Power Dissipation @ T _C = 25°C Derate above 25°C	PD	202 1.61	Watts W/°C	
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-55 to 150	°C	
Short Circuit Withstand Time (V _{CC} = 360 Vdc, V _{GE} = 15 Vdc, T _J = 25°C, R _G = 20 Ω)	t _{SC}	10	μs	
Thermal Resistance — Junction to Case – IGBT — Junction to Ambient	R _θ JC R _θ JA	0.62 45	°C/W	
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 5 seconds	TL	260	°C	
Mounting Torque, 6–32 or M3 screw	10	10 lbf•in (1.13 N•m)		

⁽¹⁾ Pulse width is limited by maximum junction temperature.

Designer's Data for "Worst Case" Conditions — The Designer's Data Sheet permits the design of most circuits entirely from the information presented. SOA Limit curves — representing boundaries on device characteristics — are given to facilitate "worst case" design.

Preferred devices are Motorola recommended choices for future use and best overall value.



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ELECTRICAL CHARACTERISTICS ($T_J = 25^{\circ}C$ unless otherwise noted)

Ch	aracteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS		•				•
Collector–to–Emitter Breakdown Voltage (VGE = 0 Vdc, I _C = 250 μAdc) Temperature Coefficient (Positive)		BVCES	600 —	 870	_	Vdc mV/°C
Emitter–to–Collector Breakdown Voltage (V _{GE} = 0 Vdc, I _{EC} = 100 mAdc)		BVECS	25	_	_	Vdc
Zero Gate Voltage Collector Current (VCE = 600 Vdc, VGE = 0 Vdc) (VCE = 600 Vdc, VGE = 0 Vdc, TJ = 125°C)		ICES	=	=	100 2500	μAdc
Gate–Body Leakage Current ($V_{GE} = \pm 20 \text{ Vdc}$, $V_{CE} = 0 \text{ Vdc}$)		IGES	_	_	250	nAdc
ON CHARACTERISTICS (1)						
Collector-to-Emitter On-State Vo (VGE = 15 Vdc, I _C = 15 Adc) (VGE = 15 Vdc, I _C = 15 Adc, T _C (VGE = 15 Vdc, I _C = 30 Adc)		VCE(on)	 - -	2.20 2.10 2.60	2.90 — 3.45	Vdc
Gate Threshold Voltage (V _{CE} = V _{GE} , I _C = 1 mAdc) Threshold Temperature Coeffici	ent (Negative)	VGE(th)	4.0 —	6.0 10	8.0 —	Vdc mV/°C
Forward Transconductance (V _{CE} = 10 Vdc, I _C = 30 Adc)		9fe	_	15	_	Mhos
DYNAMIC CHARACTERISTICS						
Input Capacitance		C _{ies}	_	4280	_	pF
Output Capacitance	$(V_{CE} = 25 \text{ Vdc}, V_{GE} = 0 \text{ Vdc}, f = 1.0 \text{ MHz})$	C _{oes}	_	275	_	
Transfer Capacitance	1	C _{res}	_	19	_	
SWITCHING CHARACTERISTICS	(1)					
Turn-On Delay Time		td(on)	-	76	-	ns
Rise Time	$(V_{CC} = 360 \text{ Vdc}, I_{C} = 30 \text{ Adc},$	t _r	-	80	_	
Turn-Off Delay Time	V_{GE} = 15 Vdc, L = 300 μH R_{G} = 20 Ω , T_{J} = 25°C)	td(off)	-	348	_	
Fall Time	Energy losses include "tail"	t _f	-	188	_	
Turn-Off Switching Loss		E _{off}	-	0.98	1.28	mJ
Turn-On Delay Time	(V _{CC} = 360 Vdc, I _C = 30 Adc, V _{GE} = 15 Vdc, L = 300 μH R _G = 20 Ω, T _J = 125°C) Energy losses include "tail"	^t d(on)	_	73	_	ns
Rise Time		t _r	_	95	_	
Turn-Off Delay Time		td(off)	_	394	_	
Fall Time		t _f	_	418	_	
Turn-Off Switching Loss		E _{off}	_	1.90	_	mJ
Gate Charge	(V _{CC} = 360 Vdc, I _C = 30 Adc, V _{GE} = 15 Vdc)	QT	_	150	_	nC
		Q ₁	_	30	_]
	- GL 19 194)	Q ₂		45		<u> </u>
NTERNAL PACKAGE INDUCTAN	CE					
Internal Emitter Inductance (Measured from the emitter lead 0.25" from package to emitter bond pad)		LE		13		nH

⁽¹⁾ Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.

TYPICAL ELECTRICAL CHARACTERISTICS

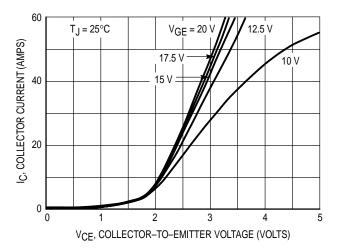


Figure 1. Output Characteristics, T_J = 25°C

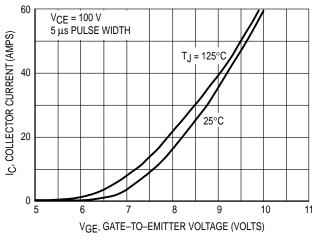


Figure 3. Transfer Characteristics

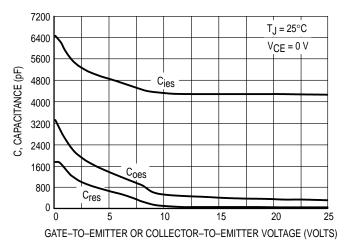


Figure 5. Capacitance Variation

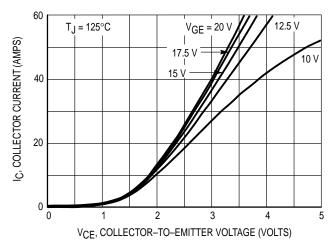


Figure 2. Output Characteristics, T_J = 125°C

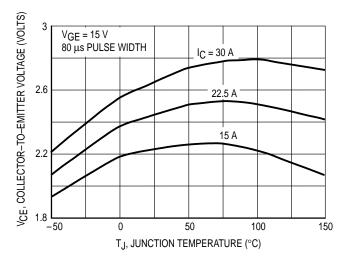


Figure 4. Collector-to-Emitter Saturation Voltage versus Junction Temperature

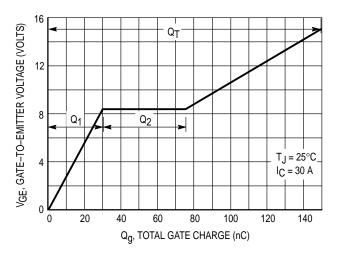


Figure 6. Gate-to-Emitter Voltage versus
Total Charge

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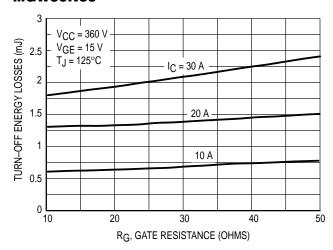


Figure 7. Turn–Off Losses versus
Gate Resistance

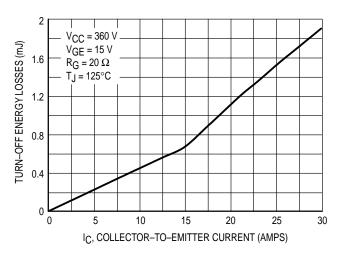


Figure 9. Turn-Off Losses versus Collector-to-Emitter Current

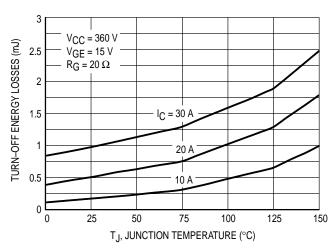


Figure 8. Turn-Off Losses versus Junction Temperature

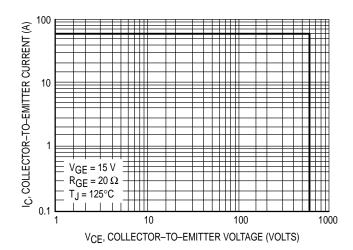
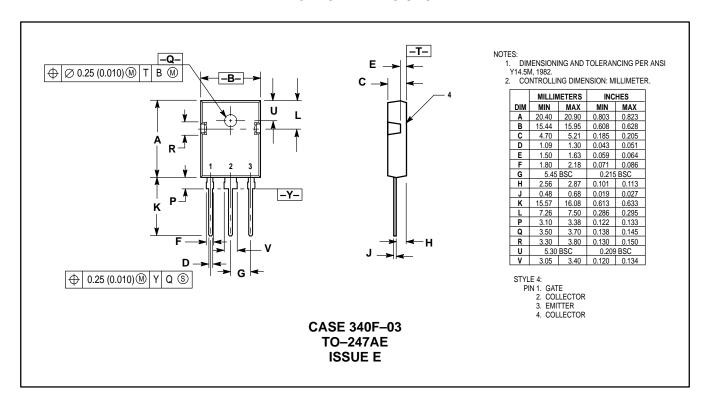


Figure 10. Reverse Biased Safe Operating Area

PACKAGE DIMENSIONS



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